

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

SASAKI *et al.*

Application Number: To be assigned

Filed: Concurrently herewith

**For: SEMICONDUCTOR INTEGRATED CIRCUIT
AND METHOD OF MANUFACTURING OF
SEMICONDUCTOR INTEGRATED CIRCUIT**

Attorney Docket No. HITA.0520

Art Unit: 2818

**Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231**

PRELIMINARY AMENDMENT

Sir:

Prior to an examination on the merits, please amend the above identified application as follows: